NOTICE OF REVISION (NOR)

(See MIL-STD-480 for instructions)
This revision described below has been authorized for the document listed.

DATE (YYMMDD) 92-02-26

Form Approved OMB No. 0704-0188

1. ORIGINATOR NAME AND ADDRESS
Defense Electronics Supply Center
Dayton, Ohio 45444-5277

2. CAGE CODE 67268

3. NOR NO. 5962-R113-92

4. CAGE CODE 67268

5. DOCUMENT NO. 5962-88582

6. TITLE OF DOCUMENT
Microcircuits, linear, bipolar hex NTDS receiver, monolithic silicon

7. REVISION LETTER
(Current) (New) A

8. ECP NO. 5962-88582ECP-1

9. CONFIGURATION ITEM (OR SYSTEM) TO WHICH ECP APPLIES
All

10. DESCRIPTION OF REVISION

Sheet 1: Revisions ltr column; add "A"
Revisions description column; add "Changes in accordance with NOR 5962-R113-92".
Revisions date column; add "92-02-26".

Sheet 4: TABLE I; For V_{OL}, delete "V_{IN} = 0.8 V", add "V_{IN} = -7.5 V (NTDS slow)" and "V_{IN} = -1.9 V (NTDS fast)". For V_{OH}, delete "V_{IN} = 2.4 V", add "V_{IN} = -4.5 V (NTDS slow)" and "V_{IN} = 1.1 V (NTDS fast)". For I_{IH}, delete "V_{IH} = 2.4 V", add "V_{IH} = -4.5 V (NTDS slow)" and "V_{IH} = -1.1 V (NTDS fast)". For I_{IL}, delete "V_{IL} = 0.8 V", add "V_{IL} = -7.5 V (NTDS slow)" and "V_{IL} = -1.9 V (NTDS fast)".

11. THIS SECTION FOR GOVERNMENT USE ONLY

a. CHECK ONE

[X] EXISTING DOCUMENT SUPPLEMENTED BY THIS NOR MAY BE USED IN MANUFACTURE.

[ ] REVISED DOCUMENT MUST BE RECEIVED BEFORE MANUFACTURER MAY INCORPORATE THIS CHANGE.

[ ] CUSTODIAN OF MASTER DOCUMENT SHALL MAKE ABOVE REVISION AND FURNISH REVISED DOCUMENT TO:

b. ACTIVITY AUTHORIZED TO APPROVE CHANGE FOR GOVERNMENT	SIGNATURE AND TITLE	DATE (YYMMDD)
DESC-ECS	Michael A. Frye BRANCH CHIEF	92-02-26
12. ACTIVITY ACCOMPLISHING REVISION	REVISION COMPLETED (Signature)	DATE (YYMMDD)
DESC-ECS	Dan Wonnell	92-02-26

REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED

THE ORIGINAL FIRST PAGE OF THIS DRAWING HAS BEEN REPLACED.

REV																			
SHEET																			
REV																			
SHEET																			

REV STATUS OF SHEET																
	REV															
	SHEET	1	2	3	4	5	6	7	8	9						

PMIC N/A

STANDARDIZED MILITARY DRAWING

THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE

AMSC N/A

PREPARED BY
Gary Zahn

CHECKED BY
Ray Monnin

APPROVED BY
Michael A. Frye

DRAWING APPROVAL DATE
23 MARCH 1989

REVISION LEVEL

DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

MICROCIRCUIT, LINEAR, BIPOLAR HEX NTDS RECEIVER, MONOLITHIC SILICON

SIZE	CAGE CODE	
A	**67268**	5962-88582

SHEET 1 OF 9

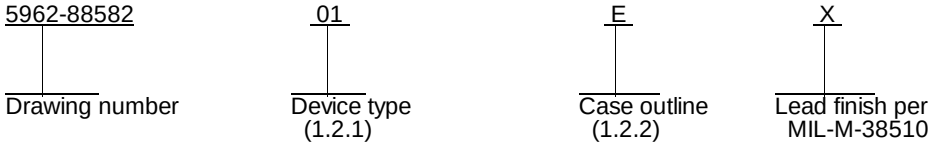
DESC FORM 193
SEP 87

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part or Identifying Number (PIN). The complete PIN shall be as shown in the following example:



1.2.1 Device type(s). The device type(s) shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	MOH0272C	Hex NTDS receiver 1/

1.2.2 Case outlines(s). The case outlines(s) shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
E	D-2 (16-lead, .840" x .310" x .200"), dual-in-line package
F	F-5 (16-lead, .440" x .285" x .085"), flat package
2	C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range	V+ = +7 V dc; V- = -16 V dc
Input voltage range	-17 V dc (V- = -16 V dc)
Storage temperature range	-65°C to +150°C
Power dissipation (P_D)	500 mW 2/
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction-to-case (O_{JC})	See MIL-M-38510, appendix C
Junction temperature (T_J)	+175°C
Output current	+5 mA

1.4 Recommended operating conditions.

Supply voltage range:	
(V+)	+4.5 V dc to +5.5 V dc
(V-)	-16.0 V dc to -13.5 V dc (slow)
(V-)	-5.5 V dc to -4.5 V dc (fast)
Ambient operating temperature range (T_A)	-55°C to +125°C
Minimum high level input voltage (V_{IH}):	
(Slow)	-4.5 V dc
(Fast)	-1.1 V dc
Maximum low level input voltage (V_{IL}):	
(Slow)	-15.0 V dc
(Fast)	-5.0 V dc

1/ This circuit was designed to be compatible with Naval Tactical Data System (NTDS) of MIL-STD-1397 "Standard Digital Navy Systems, Input-Output Interfaces", interfacing between TTL logic levels and those employed in Navy computers. The applicable specifications are designed in this drawing.
2/ Derate above T_A = +90°C, 8.33 mW/°C.

2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Case outlines(s). The case outlines(s) shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and apply over the full ambient operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in 6.4 herein.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55^{\circ}C \le T_A \le +125^{\circ}C$ V+ = 4.5 V to 5.5 V V- = -16.0 V to -13.5 V (slow) V- = -5.5 V to -4.5 V (fast)	Group A subgroups	Limits		Unit
				Min	Max	
Power supply current	I_{CC+}	No load, input ground	1, 2, 3		3.0	mA
Power supply current	I_{CC-}	No load, input ground	1, 2, 3		-5.0	mA
Low level output voltage	V_{OL}	V_{IN} = 0.8 V, I_{OL} = 4.2 mA	1, 2, 3		0.8	V
High level output voltage	V_{OH}	V_{IN} = 2.4 V, I_{OH} = -2.0 mA	1, 2, 3	2.4		V
High level input current	I_{IH}	V_{IH} = 2.4 V	1, 2, 3		0.5	mA
Low level input current	I_{IL}	V_{IL} = 0.8 V	1, 2, 3		-0.5	mA
Propagation delay time, low to high	t_{PLH}	See figures 2 and 3	9		500	ns
			1/ 10, 11			
Propagation delay time, high to low	t_{PHL}	See figures 2 and 3	9		250	ns
			1/ 10, 11			

1/ Guaranteed to the limits specified, if not tested.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (SEE 6.6 herein). The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition C using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) T_A = +125°C, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, 7, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

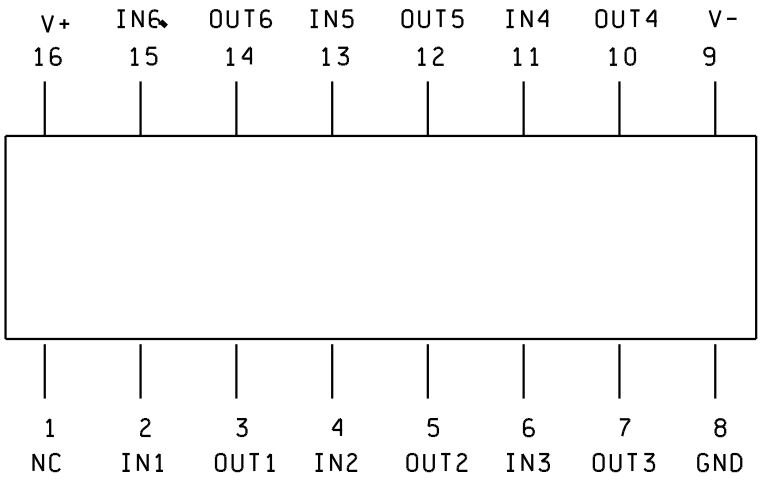
b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition C using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) T_A = +125°C, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

CASES E AND F



CASE 2

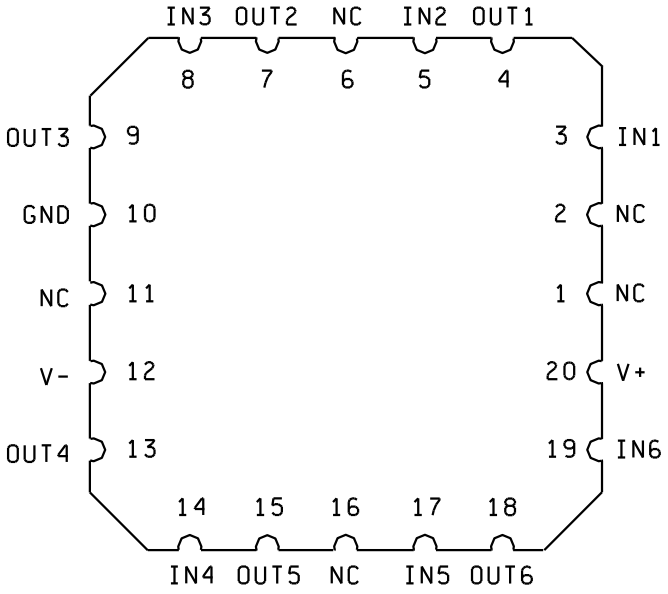


FIGURE 1. Terminal connections.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE **A**		5962-88582
		REVISION LEVEL	SHEET 6

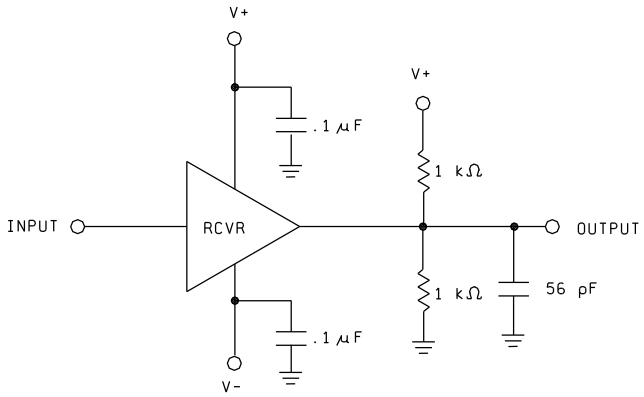
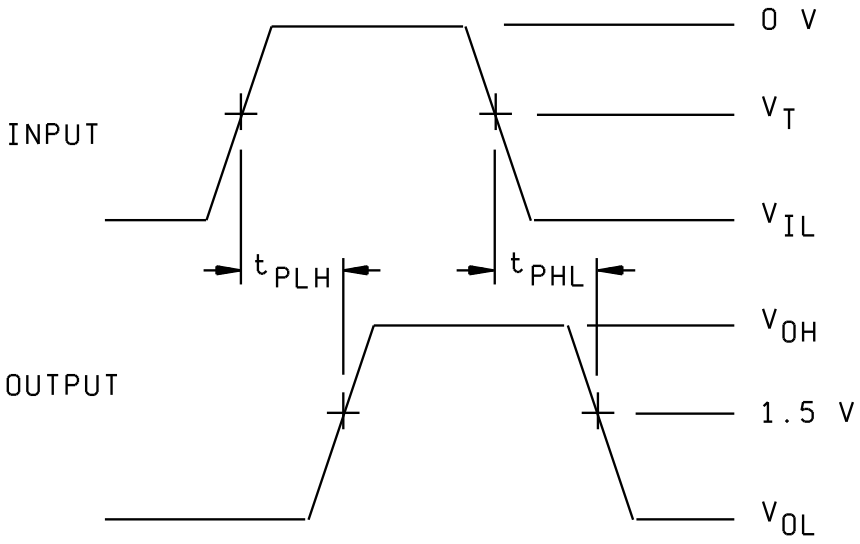


FIGURE 2. Test circuit.



NOTES:
1. For NTDS slow, V_T = -6.0 V, V_{IL} = -15 V

   For NTDS fast, V_T = -1.5 V, V_{IL} = -5 V

2. Input is a 100 Khz square wave.

FIGURE 3. AC waveforms.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	- - -
Final electrical test parameters (method 5004)	1*, 2, 9
Group A test requirements (method 5005)	1, 2, 3, 9, 10**, 11**
Group C and D end-point electrical parameters (method 5005)	1, 2, 3

* PDA applies to subgroup 1.
** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed in herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number 1/
5962-8858201EX	53469	MOH0272C-50
5962-8858201FX	53469	MOH0272C-51
5962-88582012X	53469	MOH0272C-52

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number	Vendor name and address
53469	Plessey Semiconductors Corporation 1500 Green Hills Road Scotts Valley, CA 95066